1/11	FT# 44		
16U:	U541 (Pages:	2)	Name:
Reg. No FIFTH SEMESTER B.C.A. DEGREE EXAMINATION, NOVEMBER 2018			
(CUCBCSS-UG)			
CC15U BCA5 B12 - MICROPROCESSOR AND APPLICATIONS			
(Computer Application - Core Course)			
Time	(2015 Admission ne: Three Hours	n onwards)	Maximum: 80 Marks
Time.	ic. Three flours		Maximum. 60 Marks
Part A			
Answer <i>all</i> questions. Each question carries 1 mark.			
1.	1. What is the size of instruction queue in 80	86?	
2.	2 holds the offset address of m	emory locatio	n of the next instruction to be
	executed.		
3.	3. The 8086 microprocessor hasb	oit address bus	
4.	The NMI signal of 8086 microprocessor stands for		
5.	The 8086 haslines over which address or data multiplexed.		
6.	6. The HLDA signal of the 8086 stands for_		
7.	7. The directive is used to inc	licate the start	of a logical segment.
8.	8. The BIU stands for		
9.	9. The instruction replaces the	e number in	the destination with its 2's
	complement.		
10	10. The 8255 PPI can function in mo	des.	
			$(10 \times 1 = 10 \text{ Marks})$
Part B			
Answer all questions. Each question carries 2 marks.			
11	11. What is MACRO?		
12. What are assembler directive?			
13	13. What is tri-state logic?		
14	14. What is meant by key addressing mode?		
15	15. What is DMA?		

Part C

 $(5 \times 2 = 10 \text{ Marks})$

Answer any *five* questions. Each question carries 4 marks.

- 16. How to generate the 20 bit physical address?
- 17. Explain flag registers in 8086.

- 18. Explain minimum mode of 8086 with block diagram.
- 19. What is direct addressing mode? Explaining with example.
- 20. What is interrupt vector?
- 21. What are the functions an interrupt controller?
- 22. What are the different functional units of 8253 Interval Timer?
- 23. What are the features of 80486?

 $(5 \times 4 = 20 \text{ Marks})$

Part D

Answer any *five* questions. Each question carries 8 marks.

- 24. Explain the register organization of the 8086 microprocessor.
- 25. Describe the addressing modes of 8086?
- 26. Explain working of 8251A with block diagram.
- 27. Explain the interrupt structure of 8086.
- 28. What are the different categories of instruction sets? Describe with examples.
- 29. Explain processor control instructions of 8086 with example.
- 30. Explain the functional units 8255 with block diagram.
- 31. Explain BIOS interrupts.

 $(5 \times 8 = 40 \text{ Marks})$
