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THIRD SEMESTER B.Sc. DEGREE EXAMINATION, NOVEMBER 2014

(UG-CCSS)

Computer Science—Core Course

			CS 3B 05—FUNDAMENTA	LS (OF OPERATING SYSTEMS								
	(2012 Admission onwards)												
: Th	ree	Hour	Memory.		Maximum: 30 Weightage								
Ar	Answer the following:—												
1	T	The systems which allows only one process execution at a time, are called:											
		(a)	Uniprogramming systems.	(b)	Uniprocessing systems.								
		(c)	Unitasking systems.	(d)	None of the mentioned.								
2	T	The number of processes completed per unit time is known as ———.											
3	T	The scheduling in which CPU is allocated to the process with least CPU burst time is called											
	16 What is meant by physical address ?												
4	N	Iutua	al exclusion can be provided by	the;									
		(a)	Mutex locks.	(b)	Binary semaphores.								
		(c)	Both (a) and (b).	(d)	None of the mentioned.								
5				g the	priority of processes waiting in queue for CPU								
		llocat											
6	P	roces	s synchronization canbe done	n:									
		(a)	Hardware level.	(b)	Software level.								
		(c)	Both (a) and (b).	(d)	None of the mentioned.								
7	The interval from the time of submission of a process to the time of completion is termed as:												
		(a)	Waiting time.	(b)	Turnaround time.								
		(c)	Response time.	(d)	Throughput.								
8	T	he m	emory allocation scheme subject	et to e	external fragmentation is ———.								
		(a)	Segmentation.	(b)	Swapping.								
		(c)	Pure demand paging.	(d)	Contiguous partition.								
9	V	Vhich	one of the following is the dead	llock	avoidance algorithm?								
		(a)	Banker's algorithm.	(b)	Round-robin algorithm.								
		(c)	Elevator algorithm.	(d)	Karn's algorithm.								

	10	Logica	al memo	ory is bro	ken into blocks	of th	he same size called ———.			
		(a)	Frame	s.		(b)	Pages.			
		(c)	Backin	ng store.		(d)	None of these.			
ili.	11	To avoid race condition, the maximum number of processes that may be simultaneously insid the critical section is :								
74		(a)	0.			(b)	and relugino)			
		(c)	2.			(d)	More than 2.			
	12	The ——— table contains the base address of each page in physical memory.								
rhtage		(a)	Proces	SS.		(b)	Memory.			
		(c)	Page.				Frame salwollot edu sewanA			
					na la nollacoza		$(12 \times \frac{1}{4} = 3 \text{ weight})$	age		
II.	Ans	swer al	ll questi	ons:	Uniprocessing		the sea of (a) Unipropramming systems.			
	13									
	14	Defin	e conte	xt switch	ing.		2 The number of processes completed per			
	15	The state of the s								
	16				ysical address?					
	17	What is demand paging?								
	18		e a PCI		linary semapho					
	19	What is a semaphore?								
	20									
	21				processes?					
							$(9 \times 1 = 9 \text{ weight})$	tag		
III.	An	swer a	ny five	question	offware level.		b) Gold (a) the (a) the (b)			
	22	Explain critical section problem?								
	23	Write notes on evolution of OS.								
	24	What is deadlock? What are the conditions necessary for deadlock?								
	25	What is meant by interprocess communication? Explain the two fundamental models of IP								
	26	Define deadlocks. Explain necessary conditions for deadlock.								
	27	7 How semaphore implement mutual exclusion?								
	28		t are thrithm?				heduling algorithm as compared to SJF sched	ulir		
						MI.	$(5 \times 2 = 10 \text{ weigh})$	tag		

Answer any two questions:

- 29 What is meant by scheduling? What are the different scheduling criterias? Explain any preemptive and non-pre-emptive scheduling algorithms.
- 30 Explain deadlock detection, deadlock prevention and deadlock avoidance with necessary algorithms.
- 31 What is demand paging? Explain the basic concept and performance of demand paging.

 $(2 \times 4 = 8 \text{ weightage})$