

D 91052

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Name..... **14**

Reg. No.....

FIFTH SEMESTER B.C.A. DEGREE EXAMINATION, NOVEMBER 2015

(U.G.—CCSS)

Core Course

CA 5B 08—MICRO PROCESSOR

Time : Three Hours

Maximum : 30 Weightage

I. Answer all *twelve* questions :

1. 8086 has _____ memory.
2. IP register contains _____.
3. In 8086 _____ ends a segment.
4. _____ is an example of hardware interrupt.
5. In 8086 the function of LDS reg, mem _____.
6. A 32 bit microprocessor has the word length equal to _____.
7. In a DMA write operation the data is transferred from _____ to _____.
8. All I/O devices are connected indirectly to the INTR control line, through _____.
9. _____ is a segment of code that needs to be written only once.
10. _____ is an example of logical instruction.
11. Example for value returning attribute operators is _____.
12. Putting something on stack is called _____.

(12 × ¼ = 3 weightage)

II. Answer all *nine* questions :

13. Define different type registers used in a microprocessor.
14. What are functions of flag register ?
15. What is the function of INT instruction ?
16. What is DMA ?
17. Explain different string instructions used in 8086.
18. What is meant by modular programming ?
19. Explain branch instructions in 8086.

Turn over

20. What is meant by maskable interrupt?
21. Explain indirect address mode in 8086.

(9 × 1 = 9 weigh

III. Answer any *five* questions :

22. Explain different registers used in a microprocessor.
23. Explain the concept of MACRO.
24. Explain Super scalar architecture of Pentium processor.
25. What are assembler directives ?
26. Explain Arithmetic and logic instructions used in 8086.
27. Write an 8086 program to solve the equation $(X + Y) * (2Y - Z)$. Where X, Y and Z are memory locations.
28. Write the applications of 8255.

(5 × 2 = 10 weigh

IV. Answer any *two* questions :

29. Explain 8086 interrupts and interrupt routine in detail.
30. Explain internal processor architecture of 8086 using functional block diagram.
31. Compare features of 8086, 486 and Pentium.

(2 × 4 = 8 weigh