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FIRST SEMESTER B.Sc. DEGREE EXAMINATION JANUARY 2014

(UG-CCSS)

Complementary Course - Computer Science

CS 1C 01 – COMPUTER FUNDAMENTALS AND APPLICATION PACKAGES

| ne | : Three | e Hours Maximum : 30 Weightage |
|----|---------|---|
| I. | Answ | ver all questions: |
| | 1. | The total number of digits available in the number system determines the value of its |
| | 2. | In EBCDIC, the character representation of letter S is |
| | 3. | coding scheme provides a unique number for every character independent of the platform, program and language. |
| | 4. | $(1010110)_2 + (1011010)_2 = (\underline{})_2.$ |
| | 5. | When a key is pressed on the keyboard, standard converts the keystroke into the corresponding bits. |
| | 6. | is a place where actual execution of instructions takes place during data processing. |
| | 7. | RISC is |
| | 8. | is an electronic circuit that operates on one $\it or$ more input signals to produce standard output signals. |
| | 9. | The speed of a processor directly depends on |
| | 10. | provides a screen with graphic icons or menus to give instructions to the computer. |
| | 11. | To interpret and executes various instructions, CPU uses a number of special memory units called |
| | 12. | The technology used in banking industry for faster processing of large volumes of cheques is termed as |
| | | $(12 \times \frac{1}{4} = 3 \text{ weightage})$ |

II. Answer all questions:

- 13. Define the term collating sequence.
- 14. Subtract $(011011)_2$ from $(110111)_2$ using 1's complement method.
- 15. Find the complement of the Boolean expression A . (B+C) . $(\overline{B}+\overline{C}).$
- 16. State De Morgan's laws in Boolean algebra.
- 17. What are the two main components of CPU of a computer?
- 18. What is Cache memory?
- 19. What is WORM?
- 20. What are peripheral devices?
- 21. What is a non-impact printer?

 $(9 \times 1 = 9 \text{ weigh})$

III. Answer any five questions:

- 22. Convert the following to binary: (3BC)₁₆, (534)₈, (1243)₁₀, (1.234)₈.
- 23. Multiply the binary numbers 101111 and 111.
- 24. Write the EBCDIC coding for the word 'HIT' in both binary and hexadecimal nota How many bytes are required to store this word using EBCDIC?
- 25. Construct the logic circuit diagram for the Boolean expression by using NAND only: $(\overline{A} + \overline{B})$. (A + C). $(B + \overline{C})$.
- 26. Express the Boolean function $F = A + \overline{B}$. C in sum-of-products form.
- 27. Differentiate between random access and sequential access storage units.
- 28. What are the limitations of image scanner? .

 $(5 \times 2 = 10 \text{ weigh})$

IV. Answer any two questions:

- 29. Write short notes on:
 - (a) Unicode.
 - (b) Canonical forms of Boolean functions.
 - (c) Combinational circuits.
 - (d) Storage evaluation criteria.
- 30. With the help of a neat diagram, explain the processor and memory architectur computer system.
- 31. Explain any two input and output devices.